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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Akihiko EBINA, Yutaka MARUO

Application No.: New U.S. Patent Application

Filed: January 23, 2002

4 / IDS
E. Willis
7-17-02

Docket No.: 111778

For: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR MEMORY DEVICES

INFORMATION DISCLOSURE STATEMENT

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- 2. Relevance of the non-English language reference 12 is discussed in the present specification.
- 3. English translations of references 9-11 are attached hereto.

Respectfully submitted,


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